

FIG. 2A is a schematic diagram of a system 100 for generating a signal. The system 100 includes a processor 102, a memory 104, and a transmitter 106. The processor 102 is configured to generate a signal and control the transmitter 106 to transmit the signal. The memory 104 is configured to store data and instructions. The transmitter 106 is configured to transmit the signal over a communication channel 108. The system 100 may be implemented in a variety of devices, including a mobile phone, a tablet, a laptop, or a server.

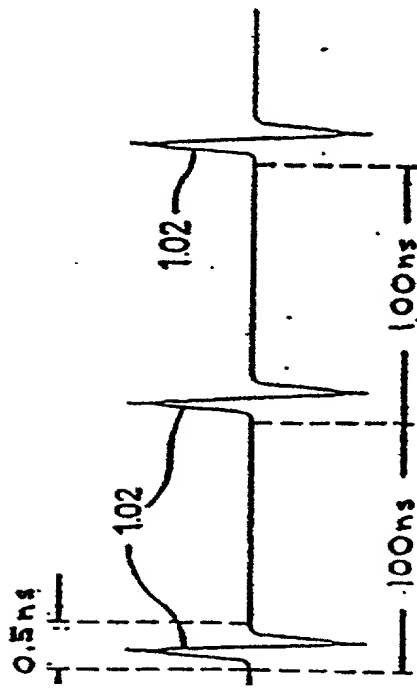


FIG. 2A

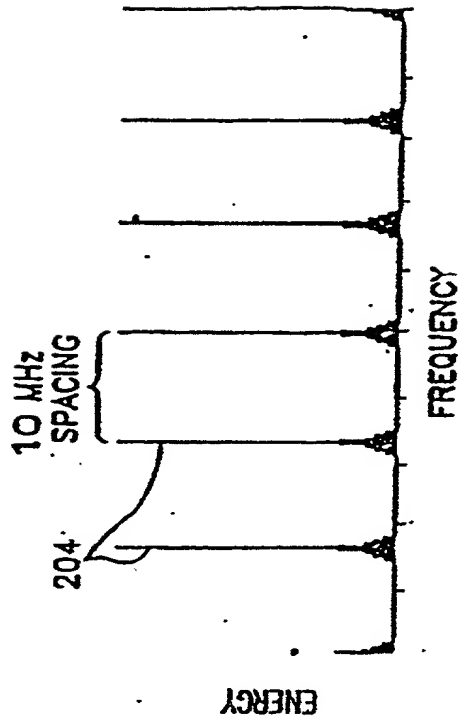


FIG. 2B

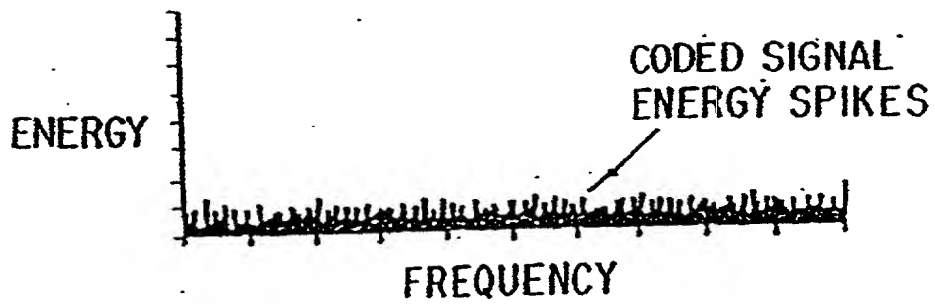
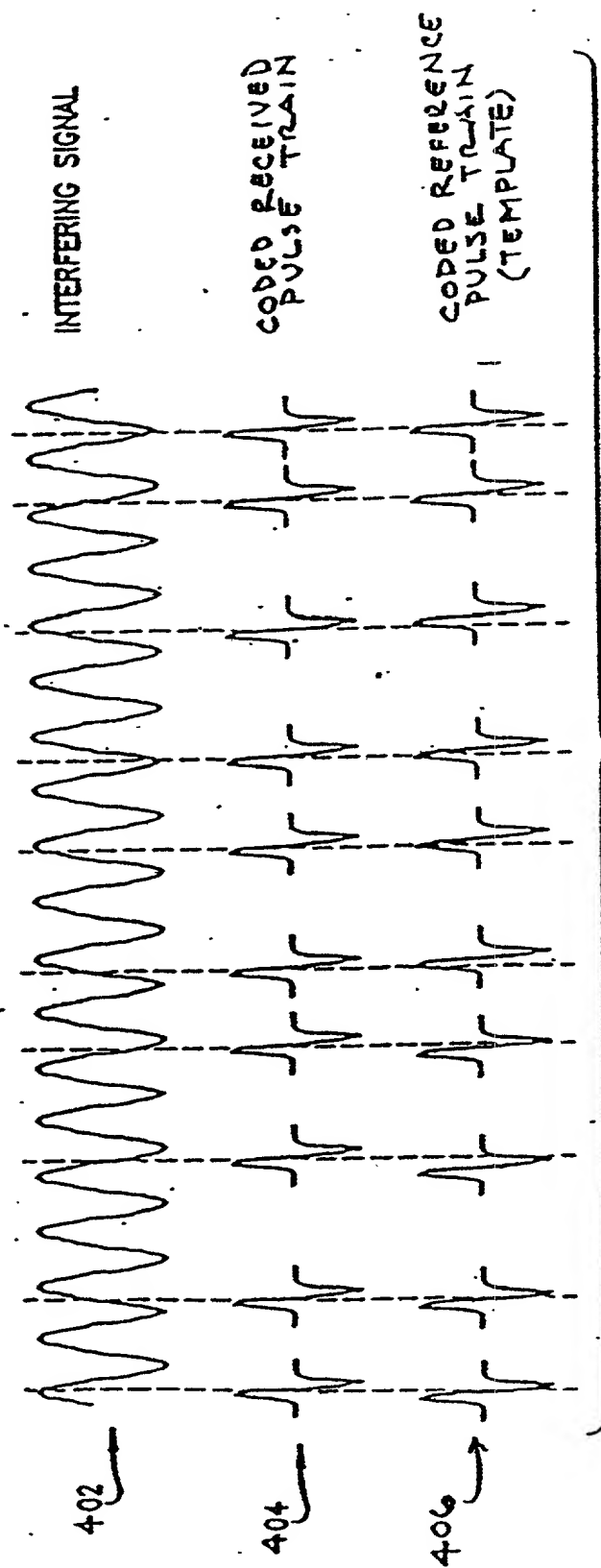


FIG. 3



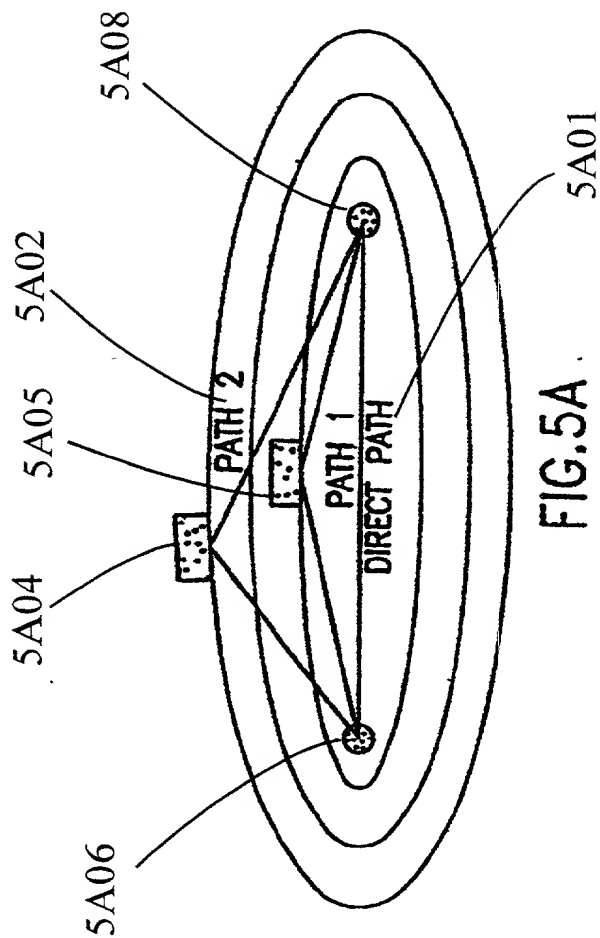


FIG. 5A

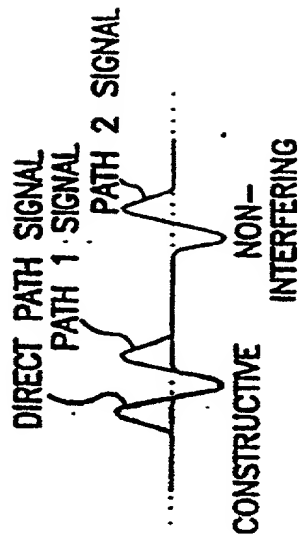
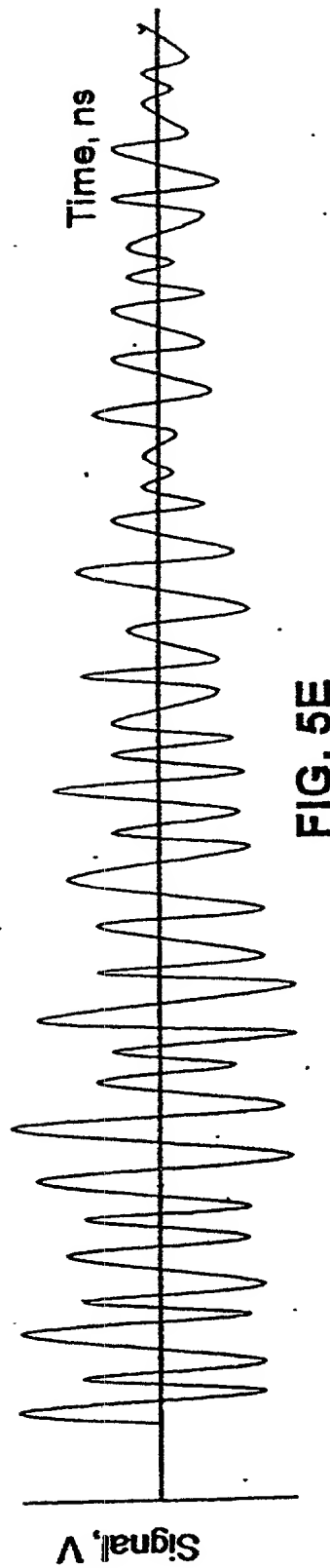
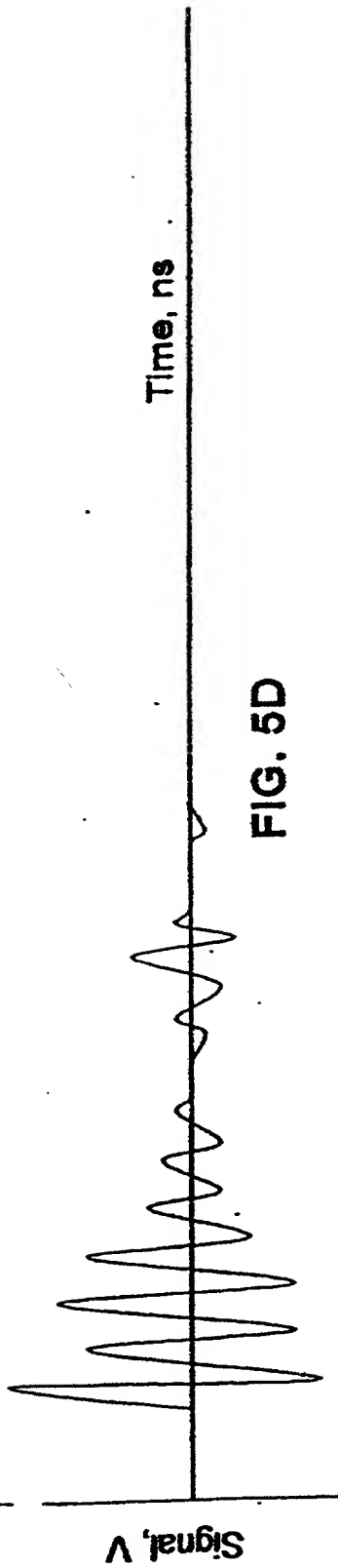
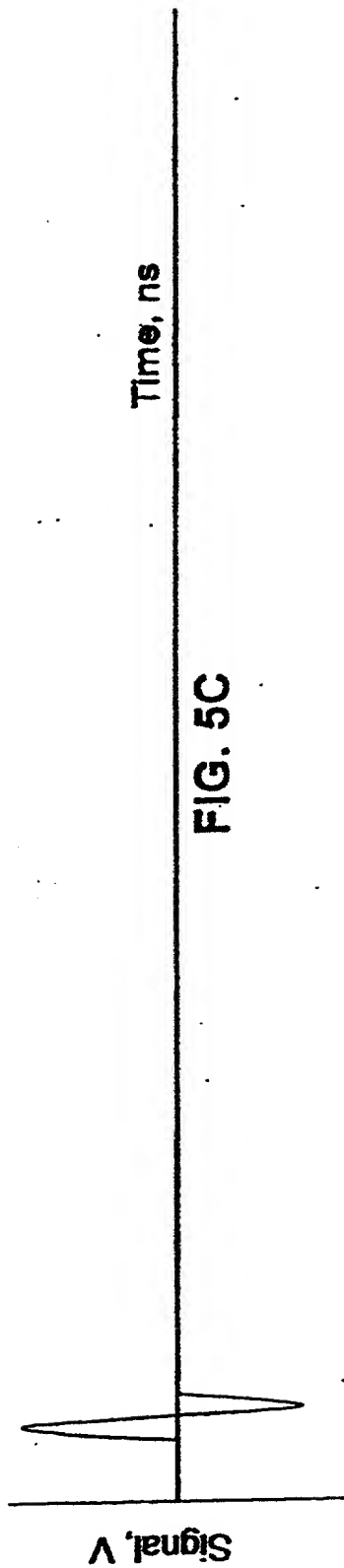


FIG. 5B



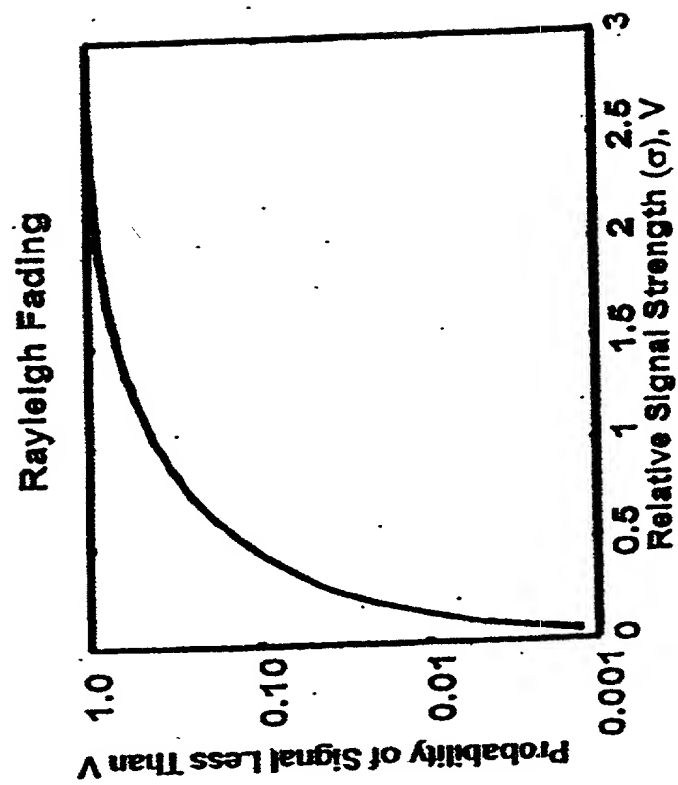


Fig. 5

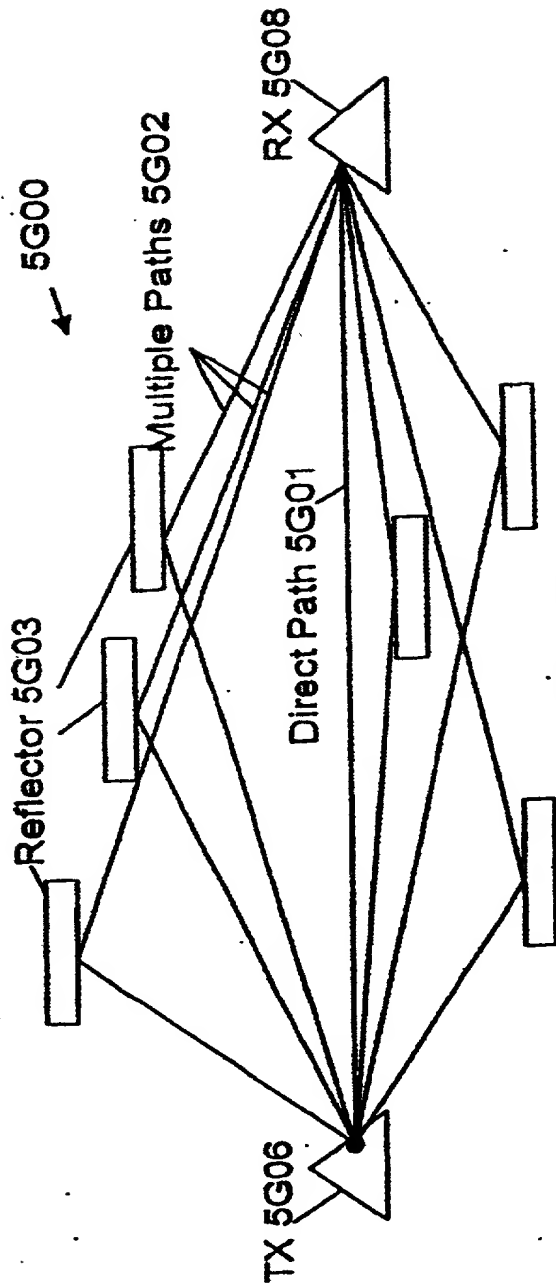


FIG. 5G

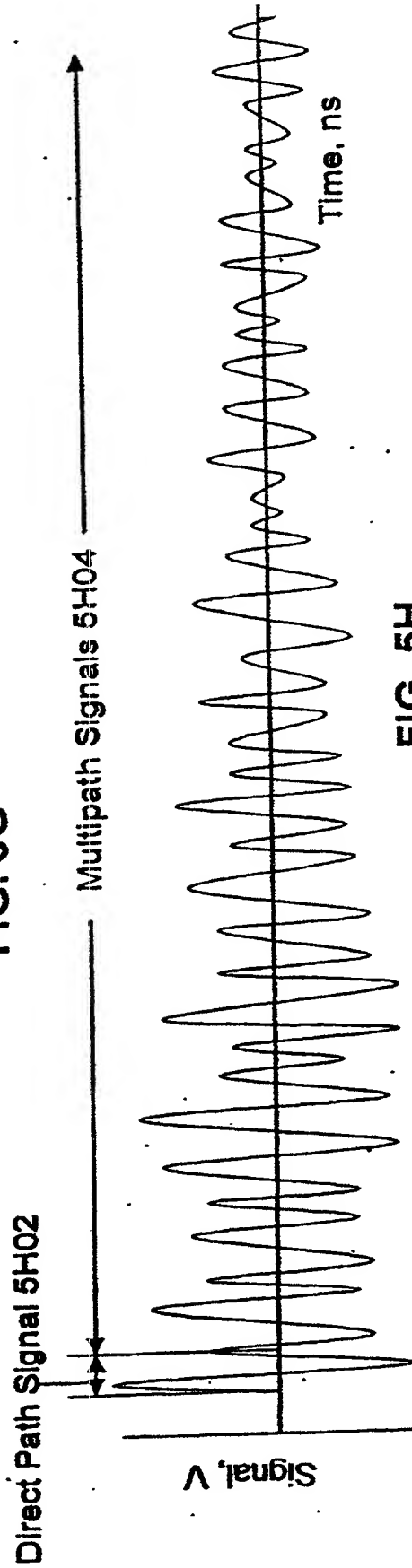


FIG. 5H

FIG. 7

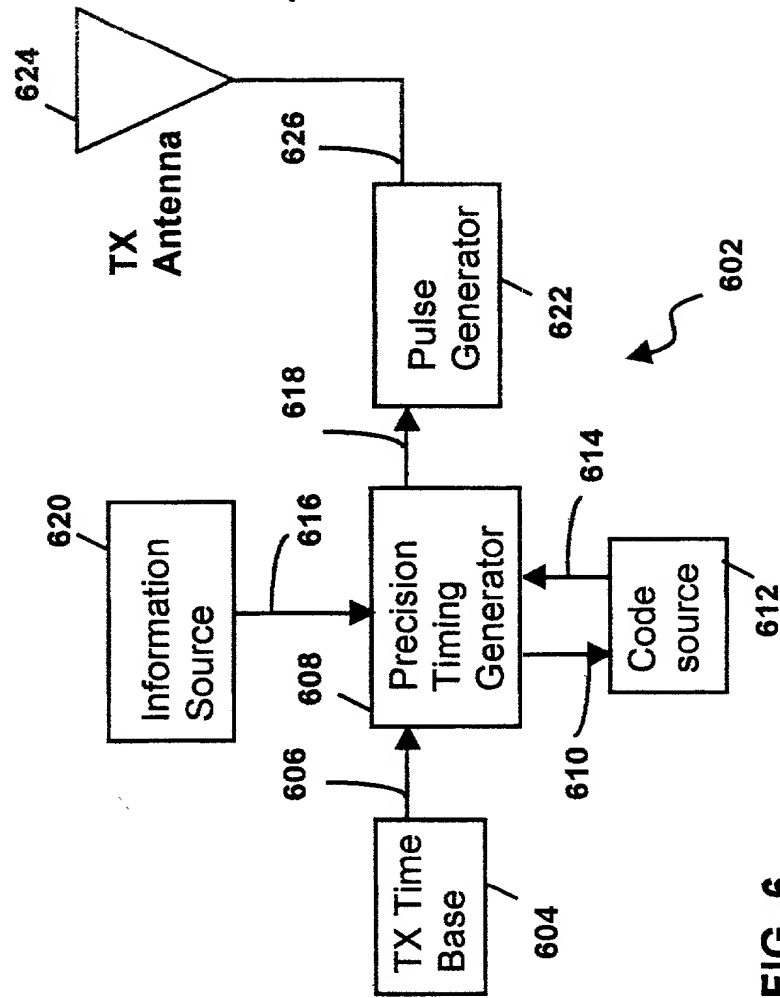
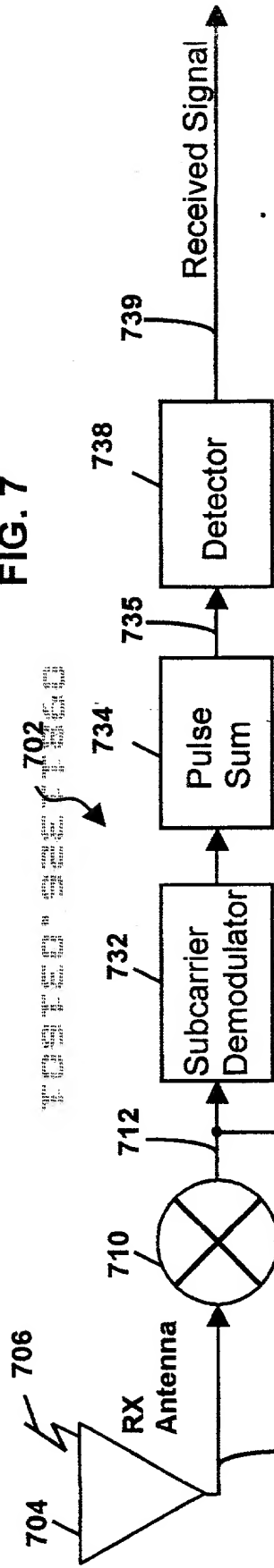


FIG. 6

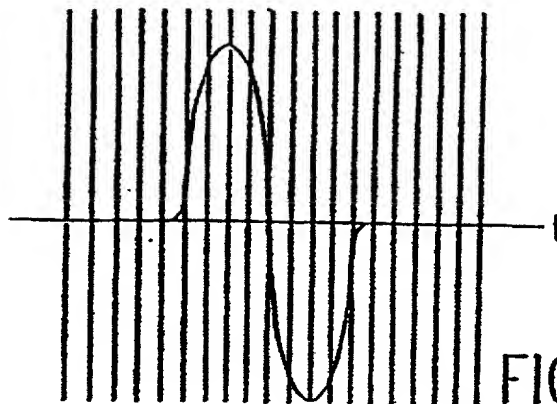


FIG. 8A

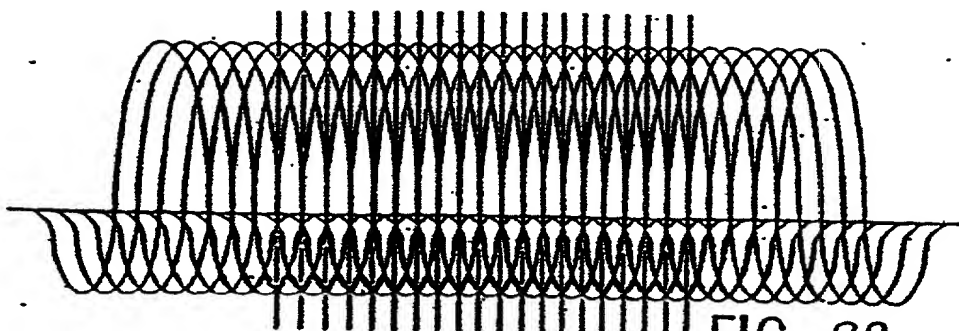
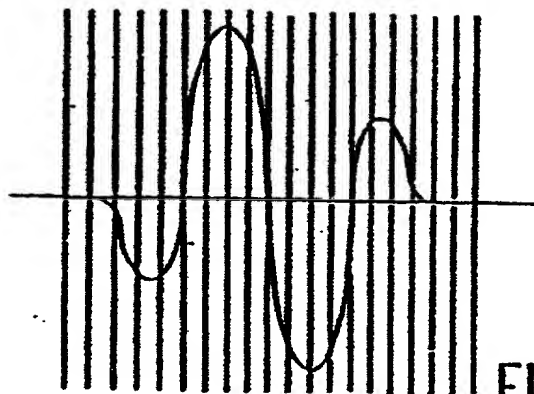


FIG. 8B



CORRESPONDING
TO EACH
 Δl

FIG. 8C

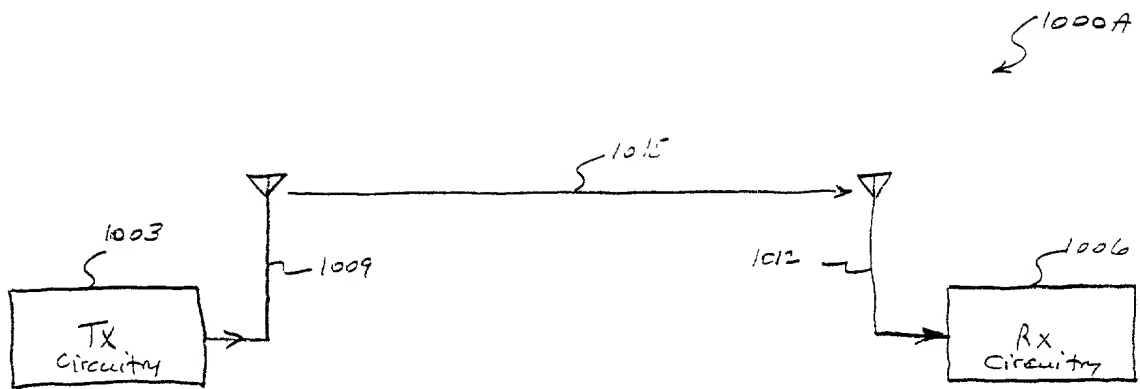


FIG. 9

FIG. 10A

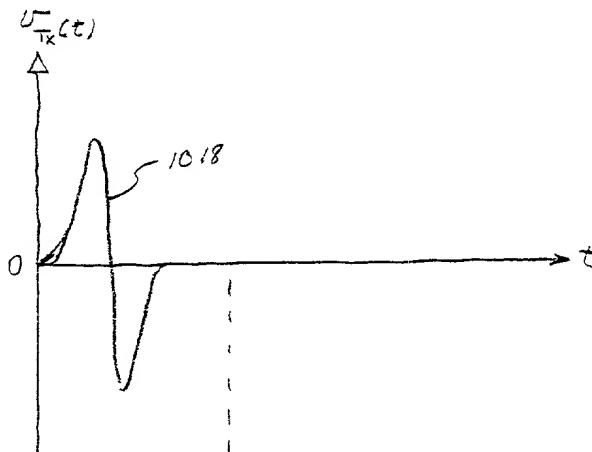
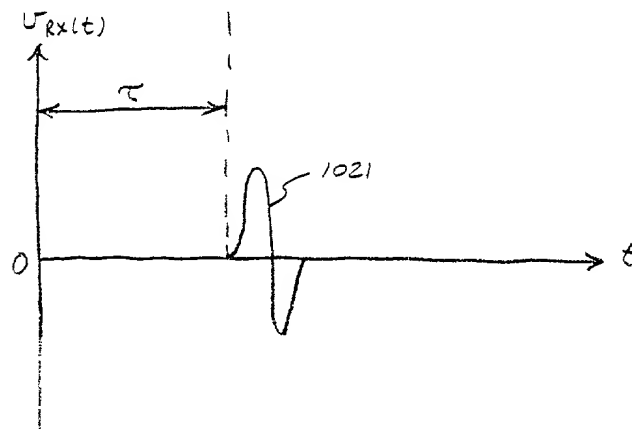


FIG. 10B



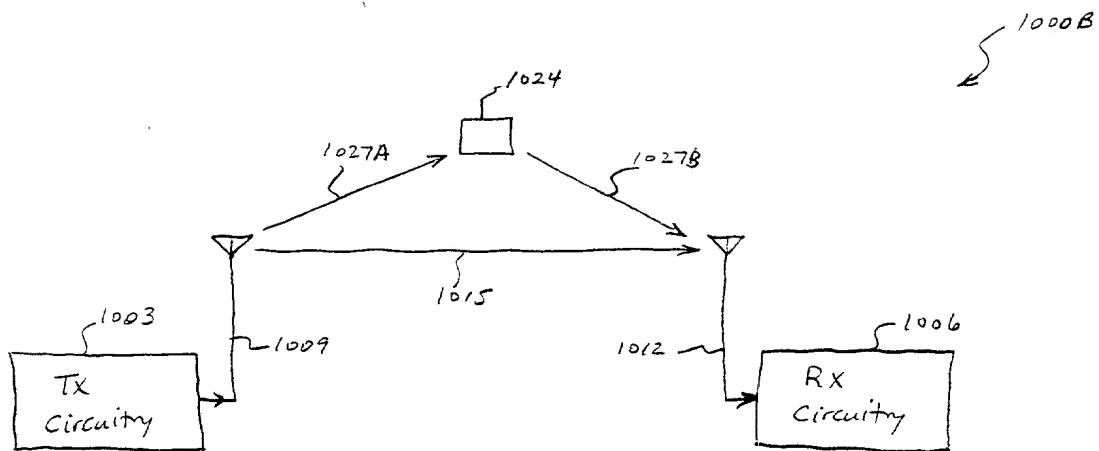


FIG. 11

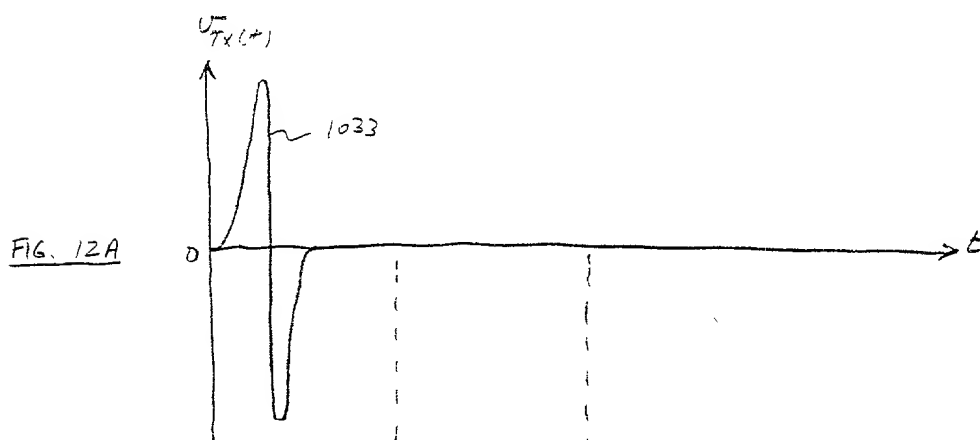


FIG. 12A

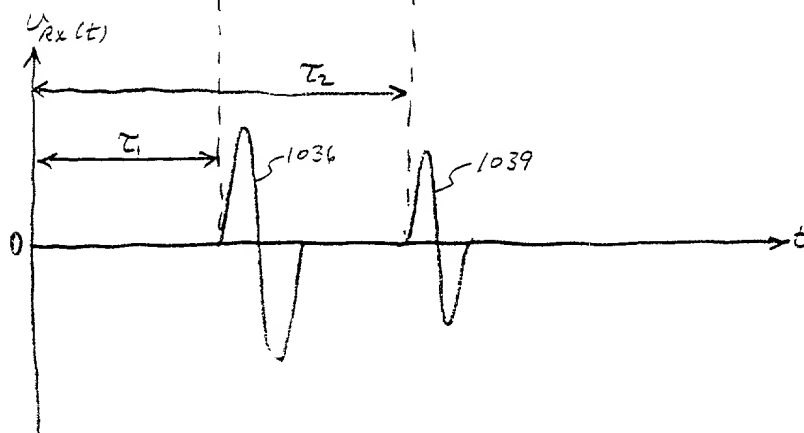


FIG. 12B

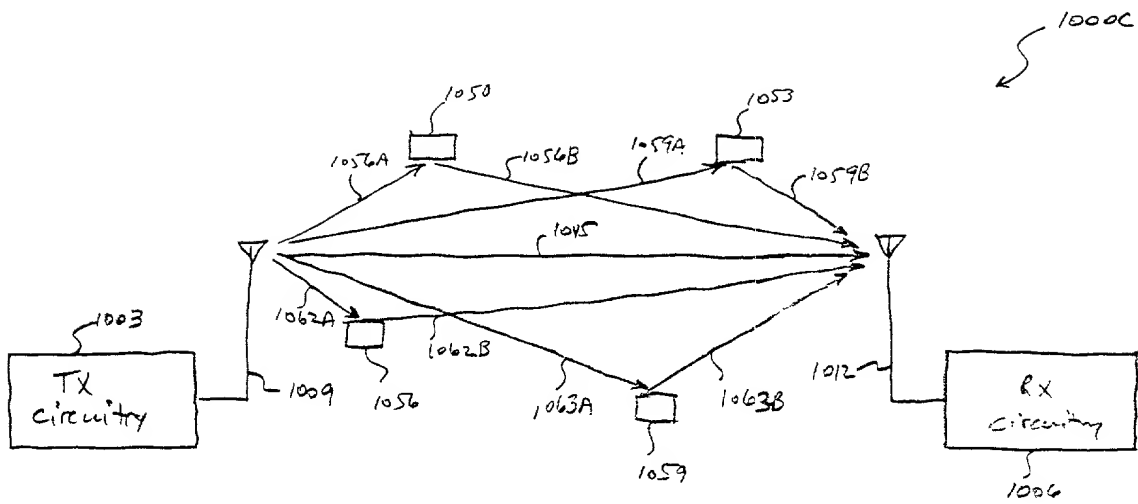
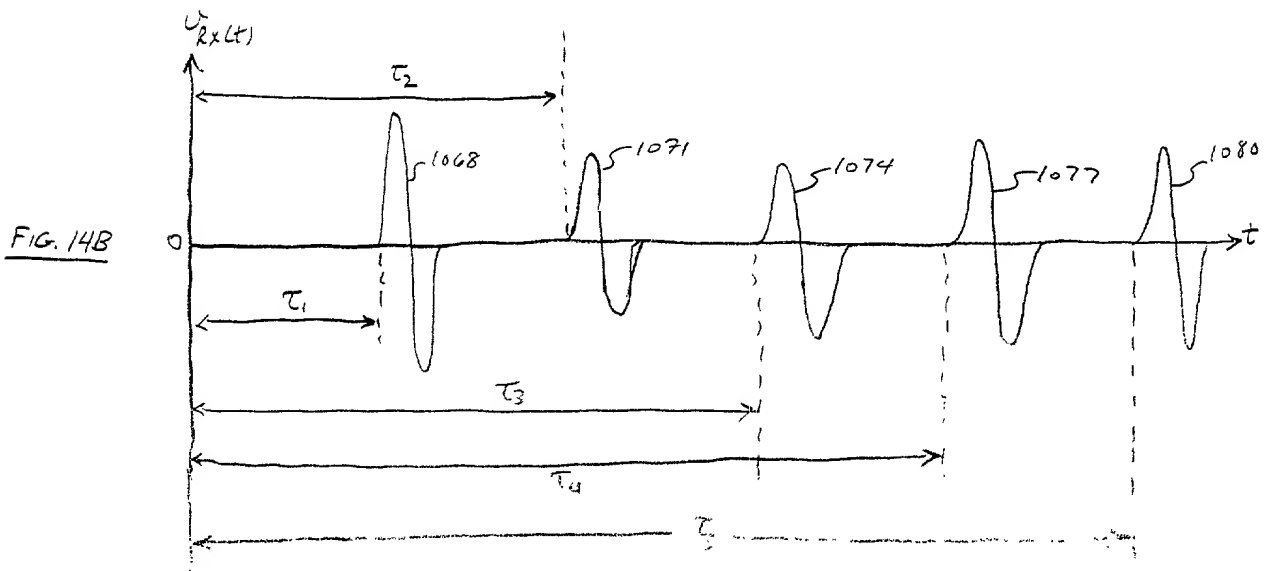
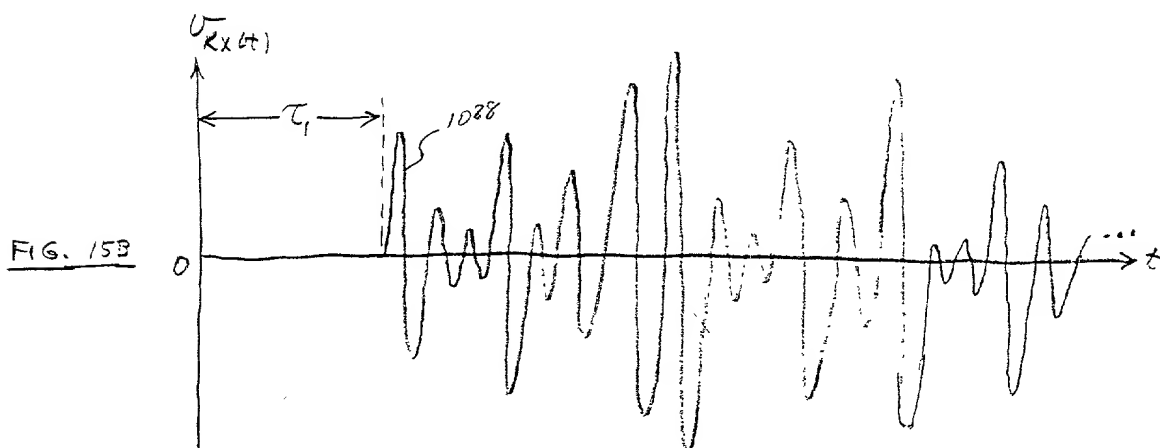
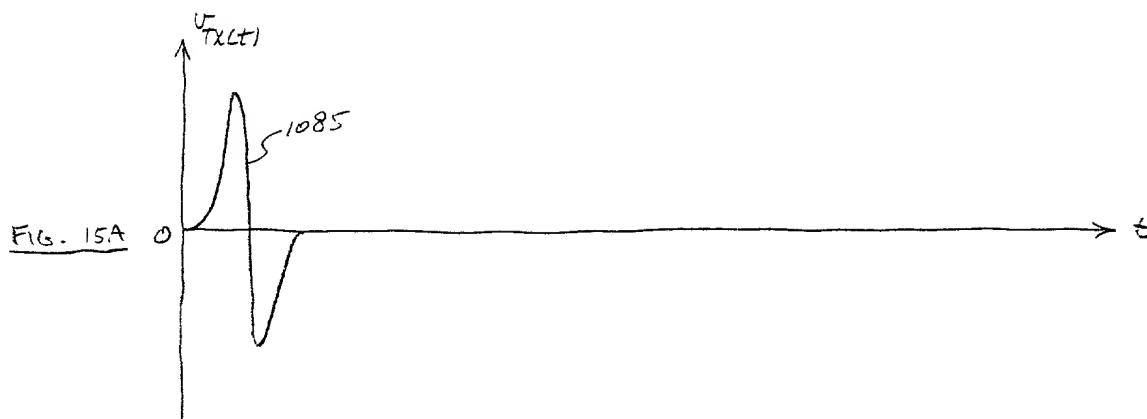


FIG. 13





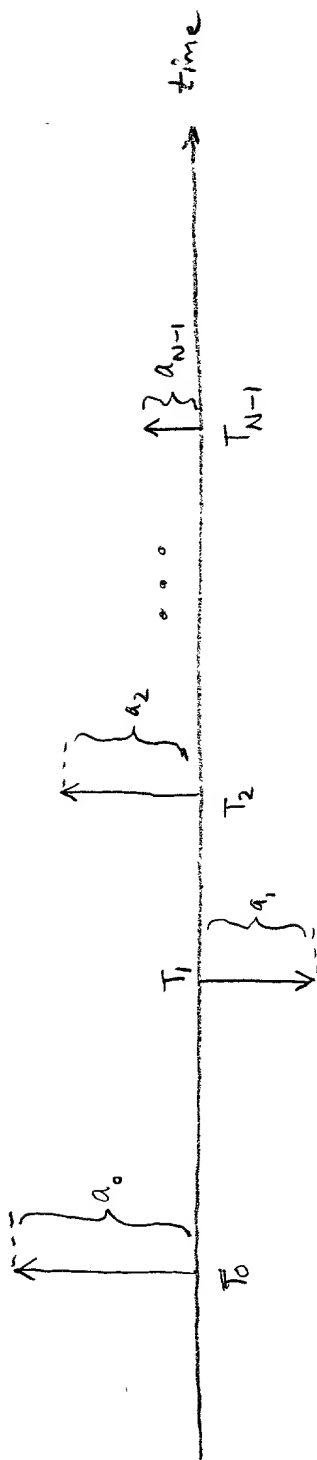


FIG. 16

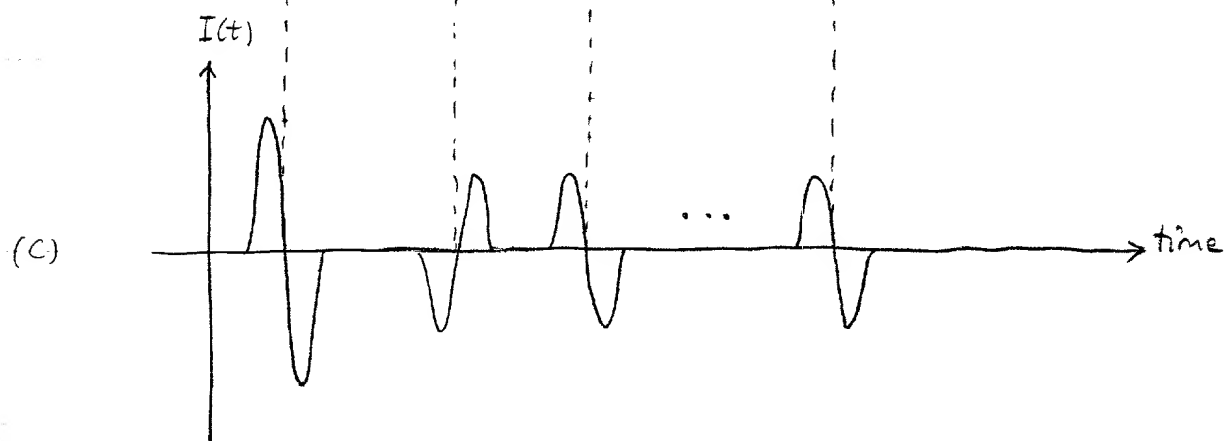
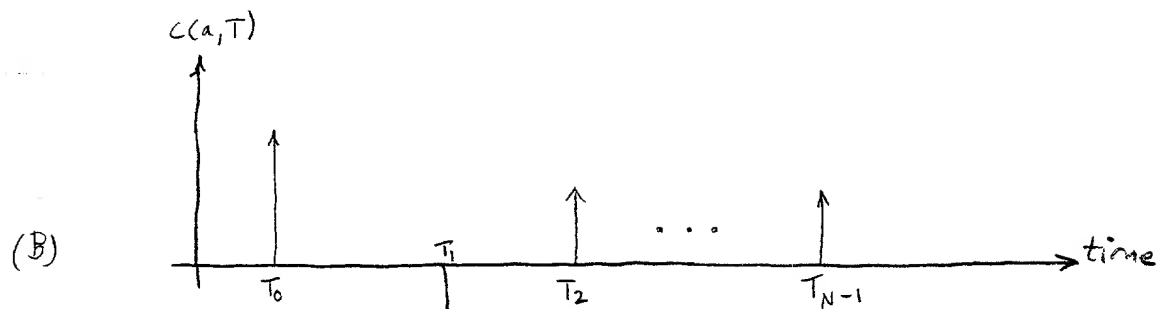
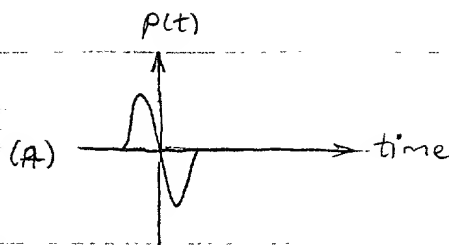


FIG. 17

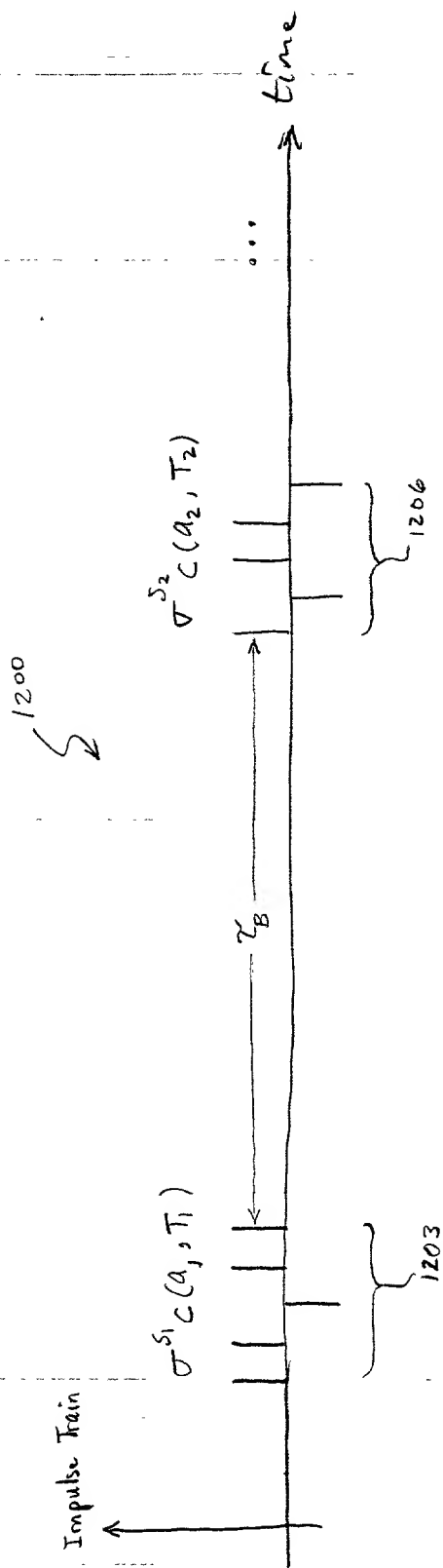


FIG. 18

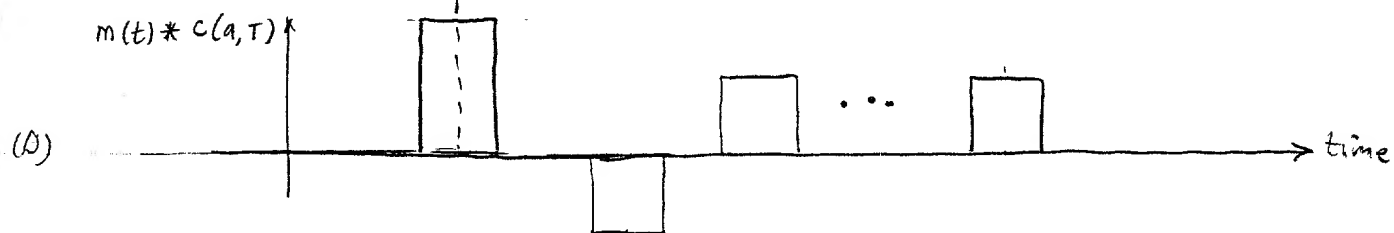
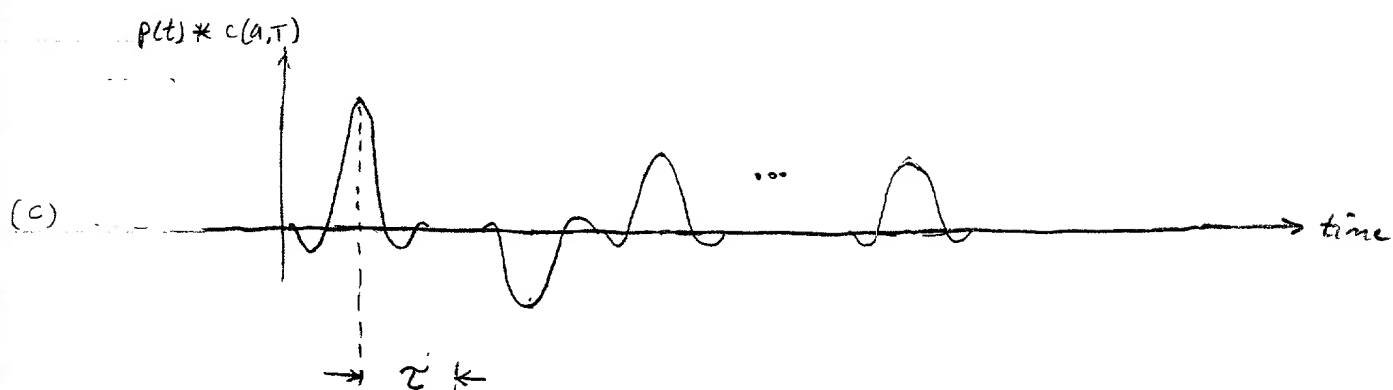
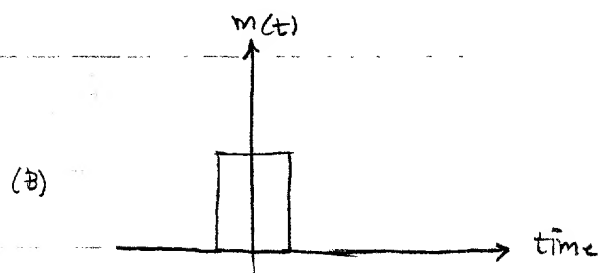
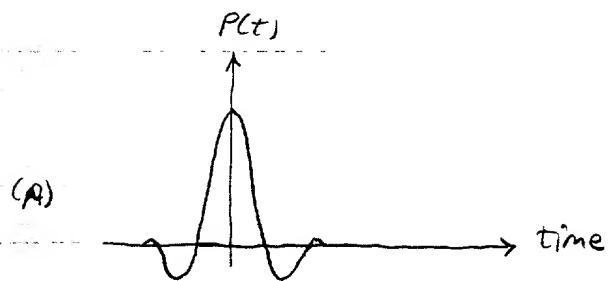


FIG. 19

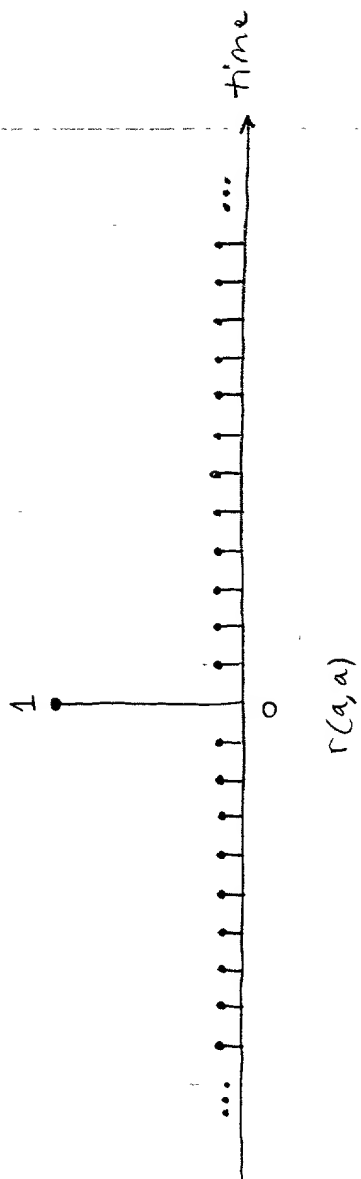
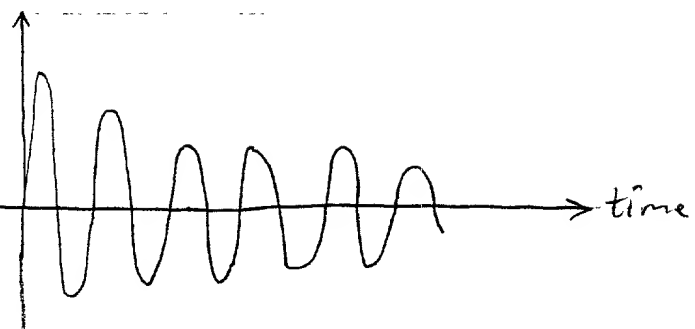


FIG. 20

$p(t)$



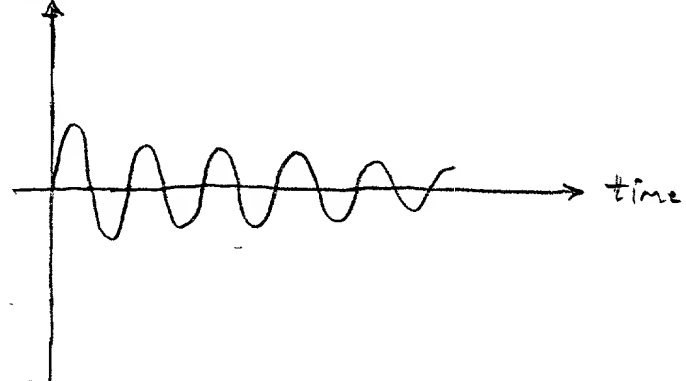
(A)

$m(t)$



(B)

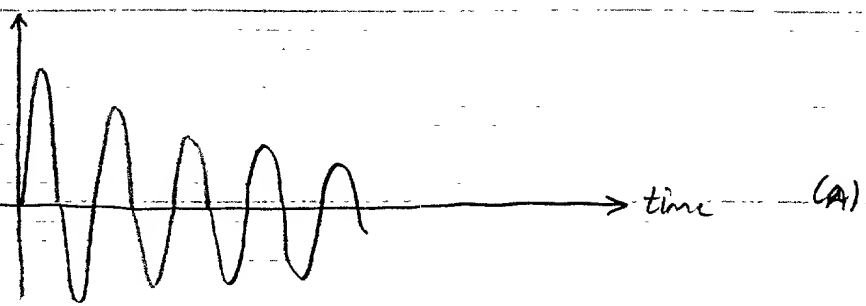
$R(p(t), m(t))$



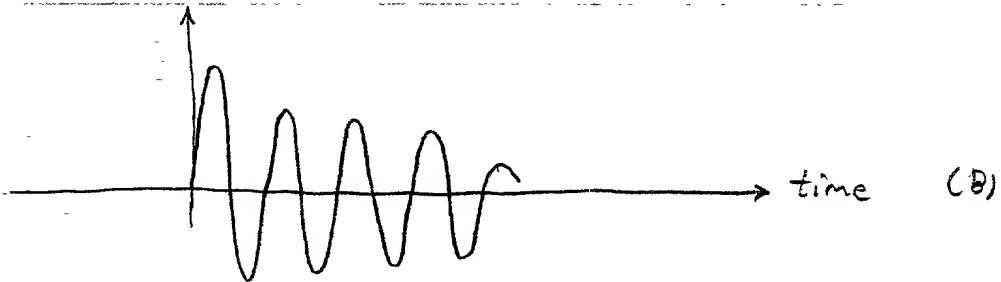
(C)

FIG. 21

$p(t)$



$m(t)$



$R(p(t), m(t))$

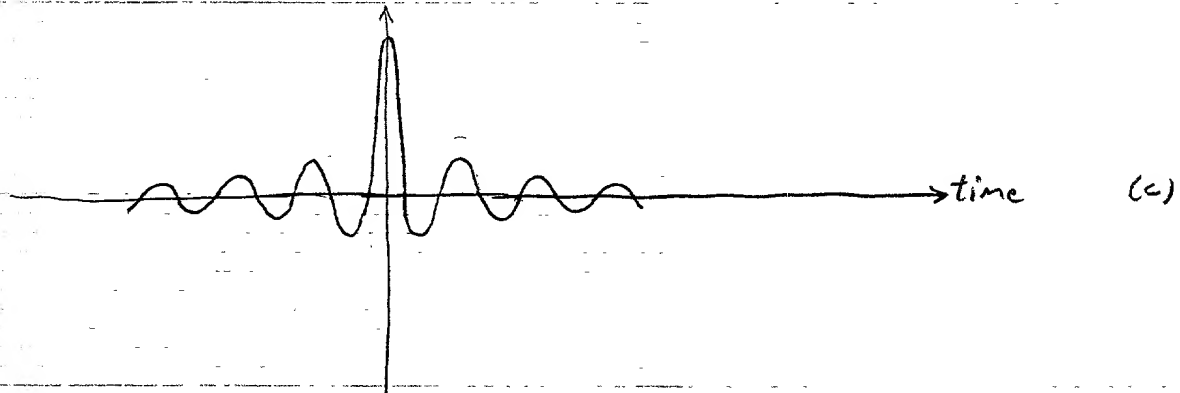


FIG. 22

Figure 23 shows a sequence of pulses in time. The pulses are represented by vertical lines of varying heights. The time axis is labeled 'time' and has tick marks at $-T$, 0 , and T . The pulses are centered at $-T$, 0 , and T . The height of the pulses is indicated by the label $C_B(a, T)$ on the left.

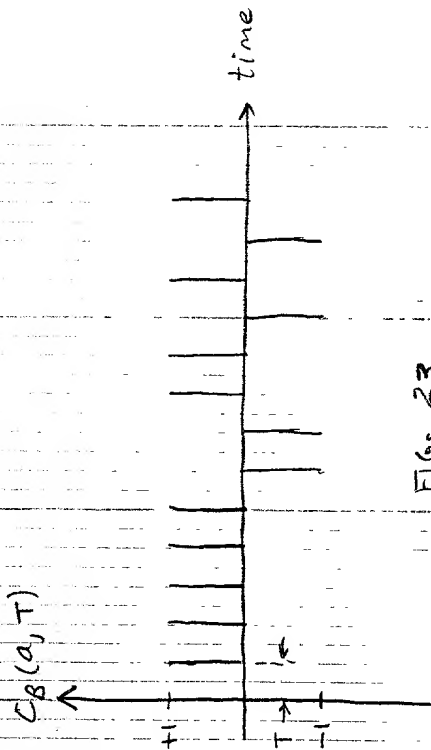


FIG. 23

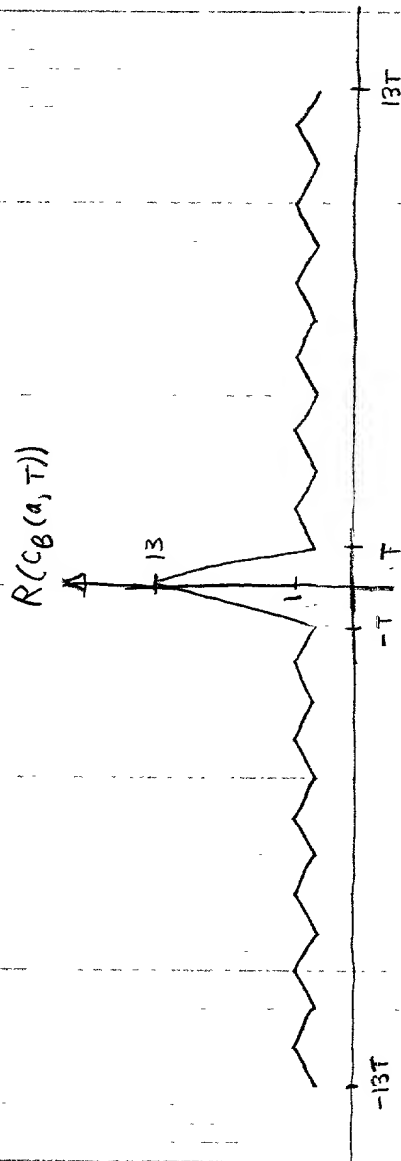


FIG. 24

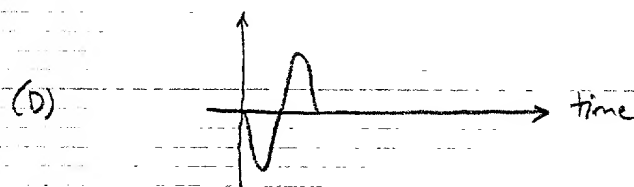
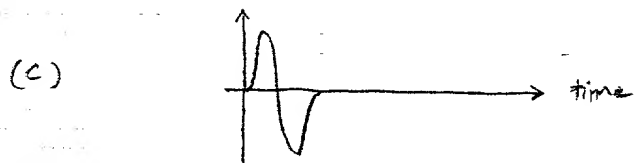
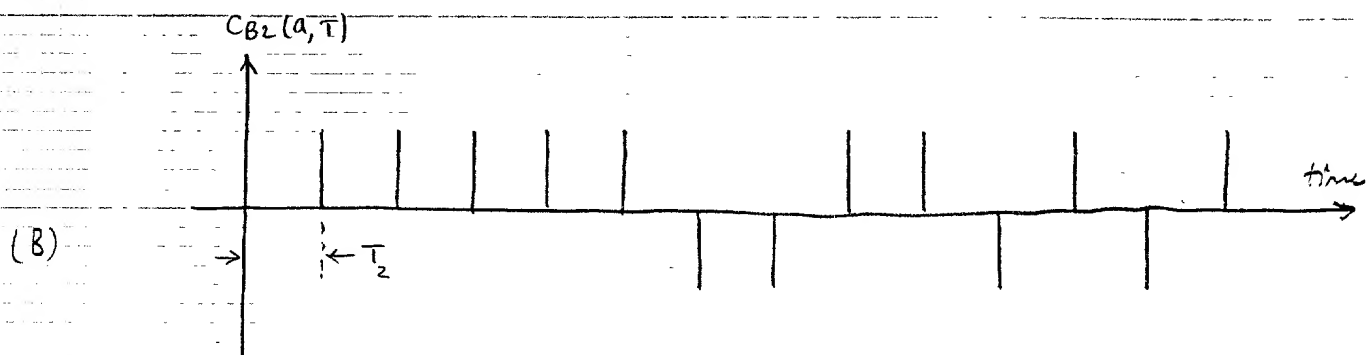
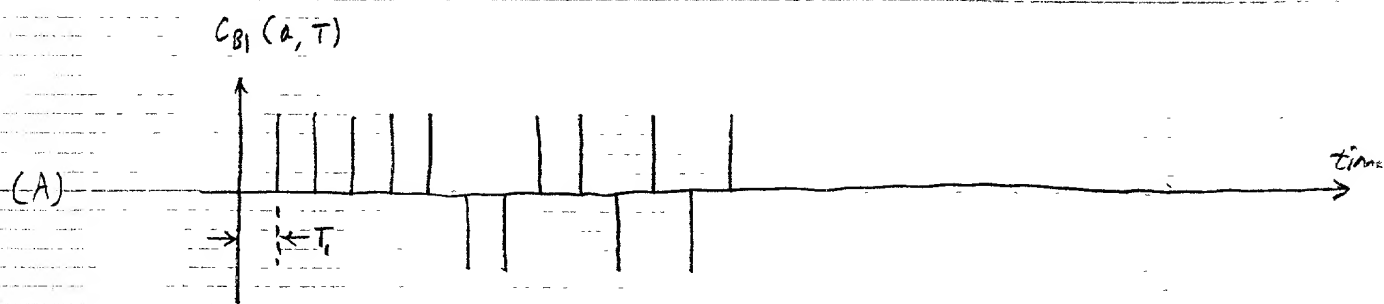


FIG. 25

2000 2003 2006 2009

2000

2003

2006

2009

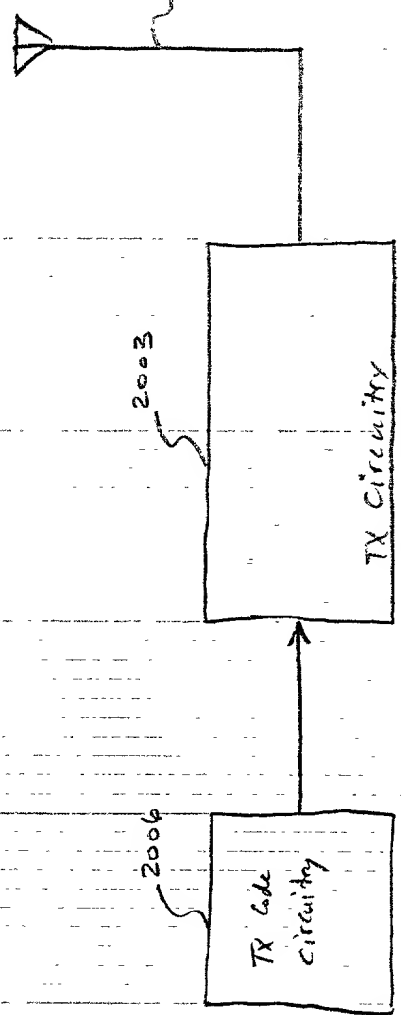


FIG. 26

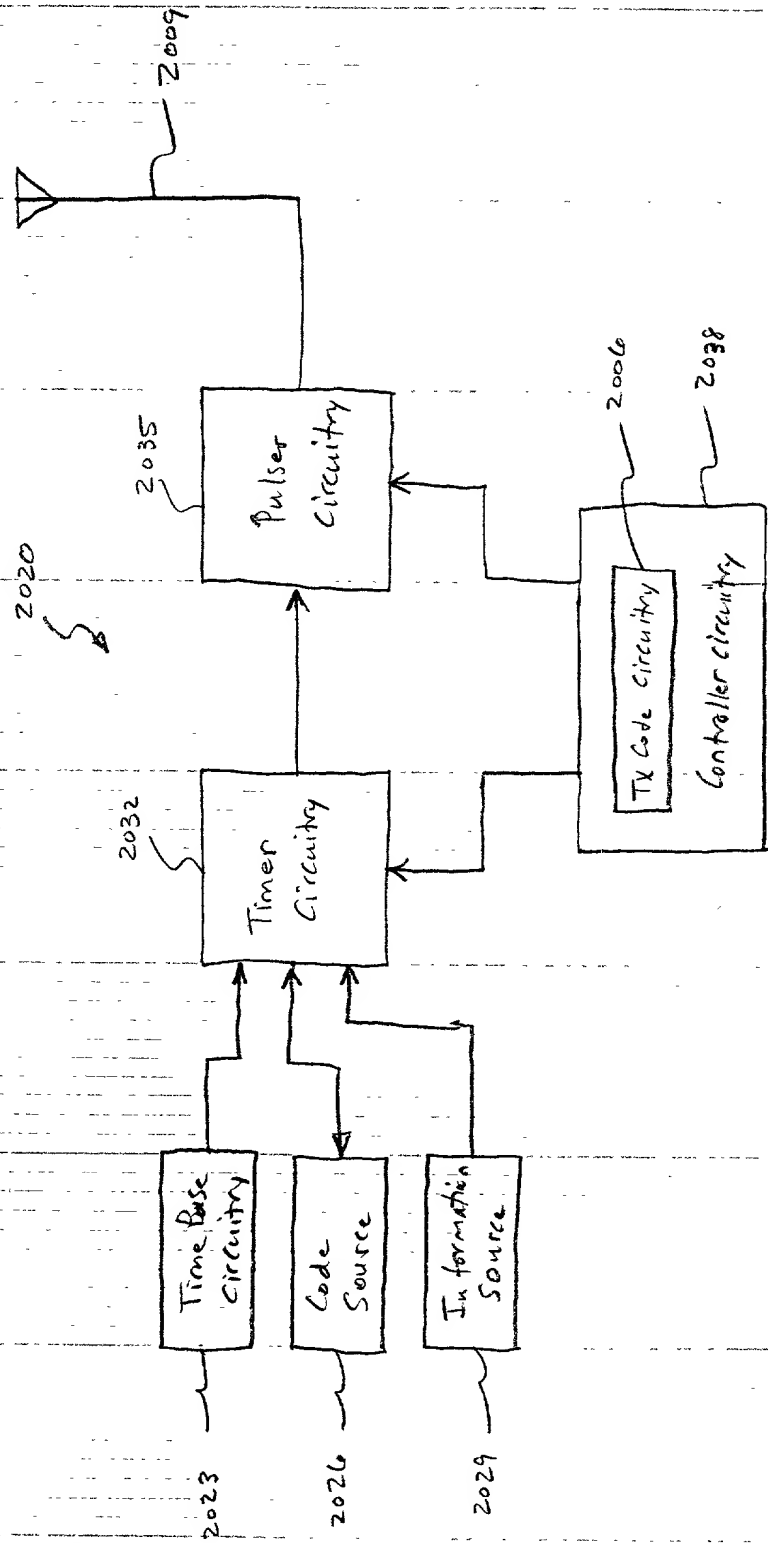


FIG. 27A

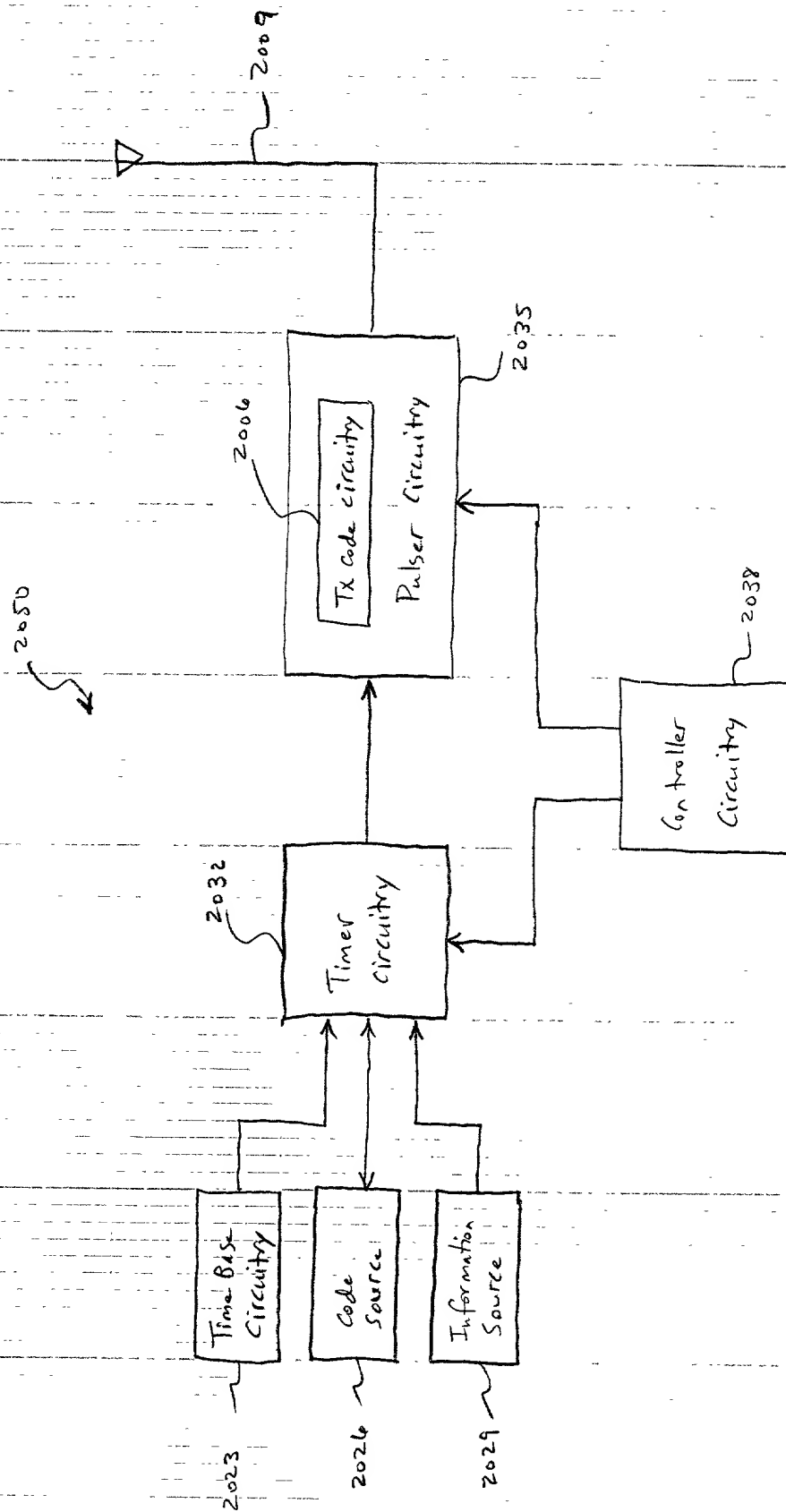


FIG. 27B

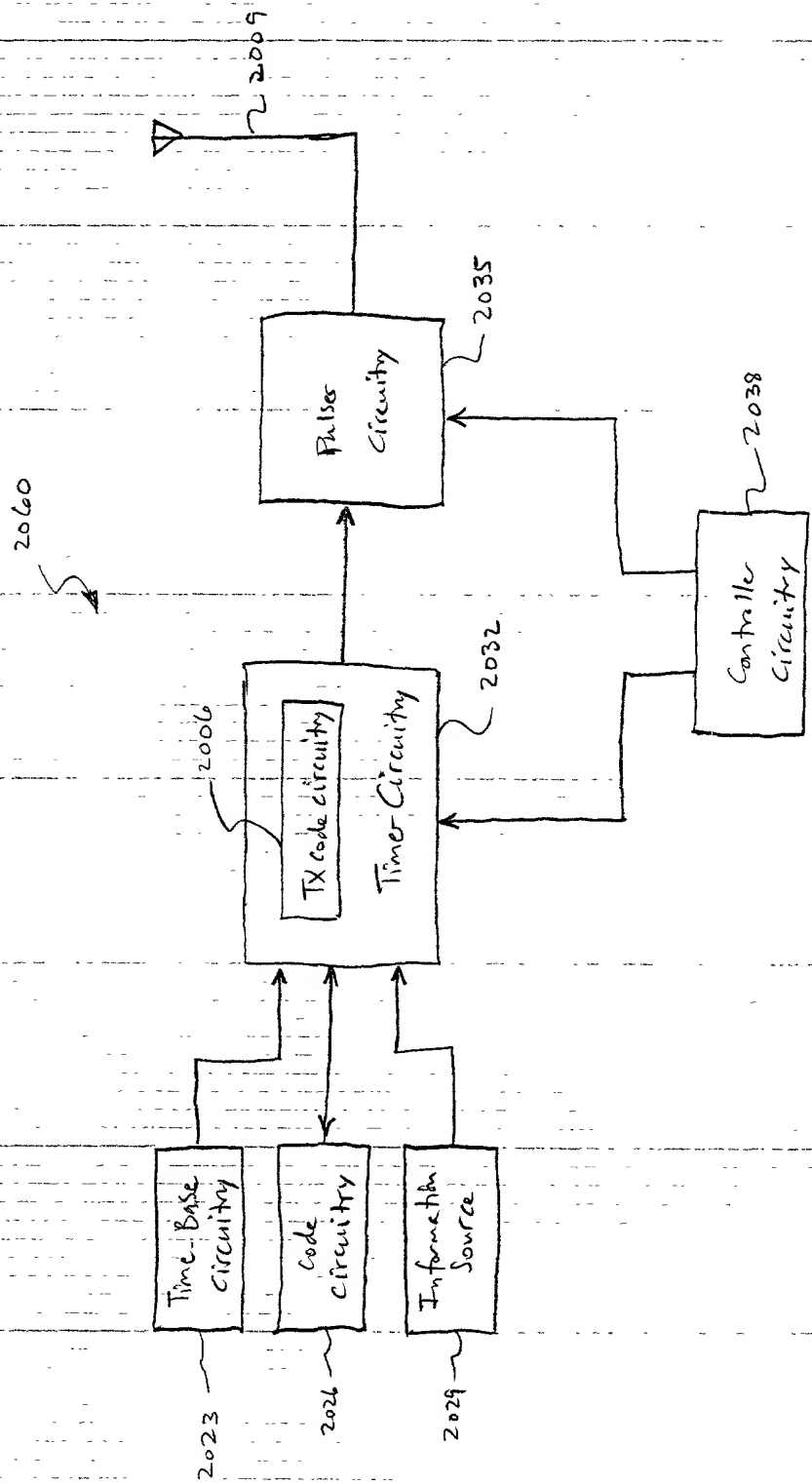


FIG. 27C

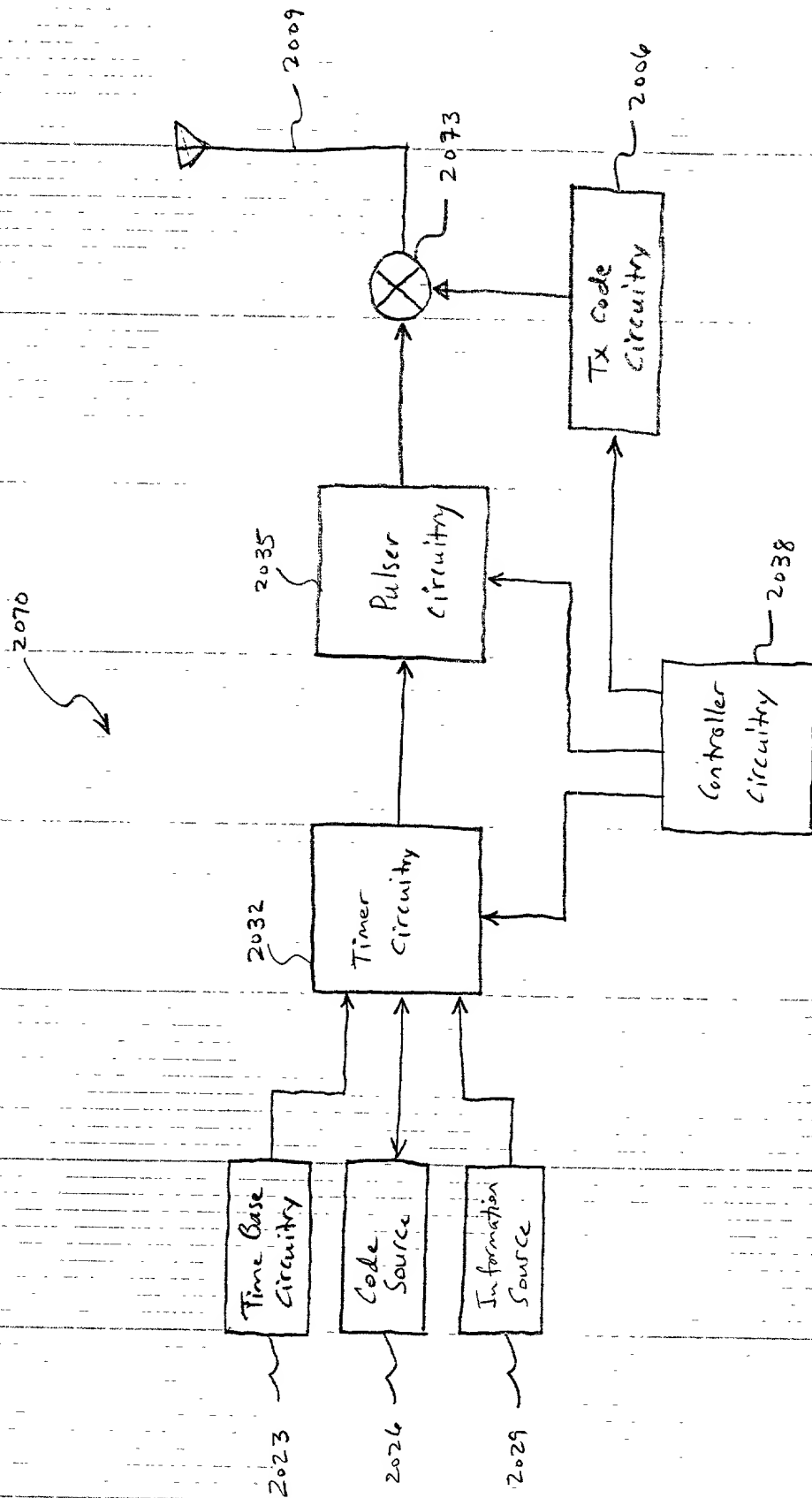


FIG. 27D

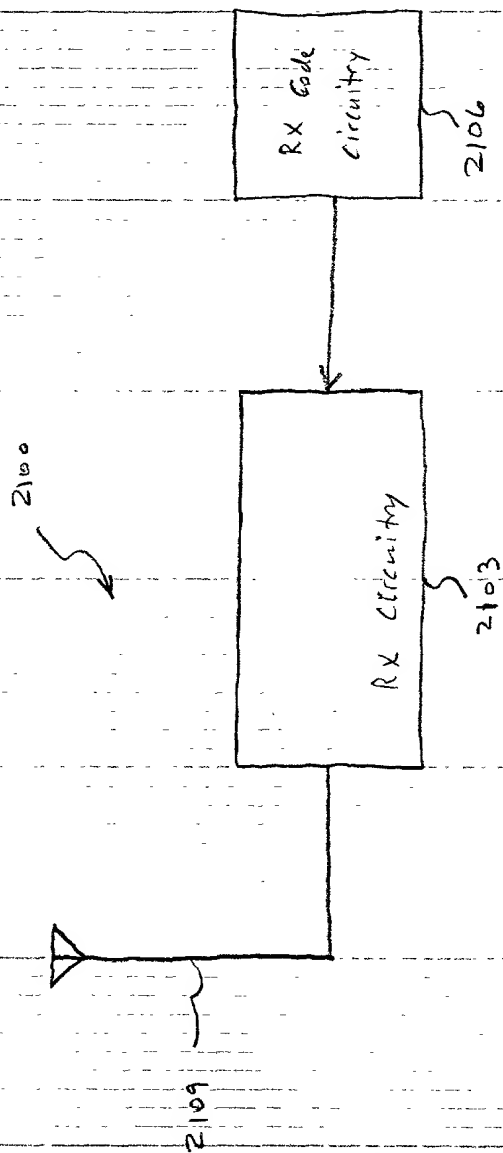


FIG. 28A

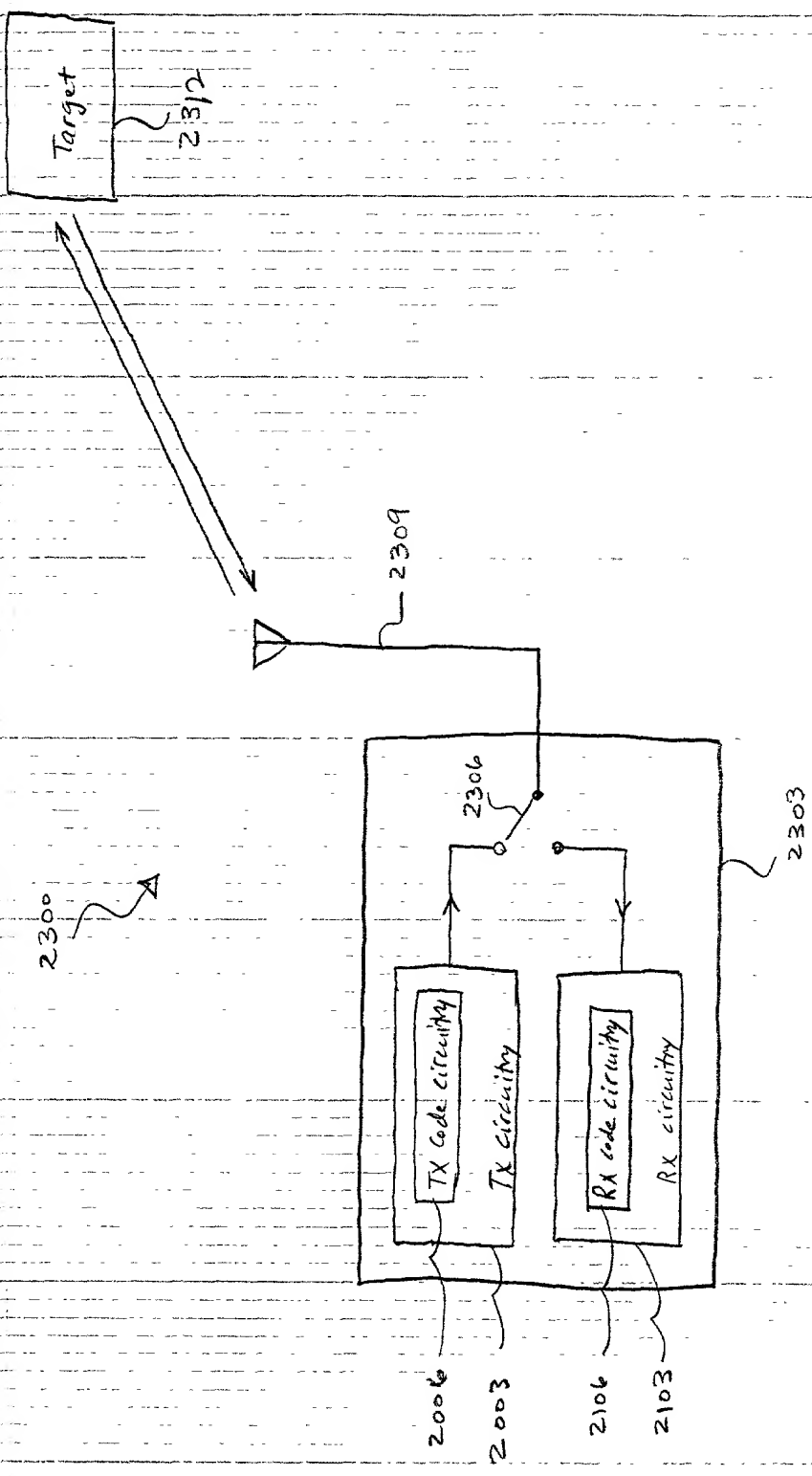


FIG. 29A

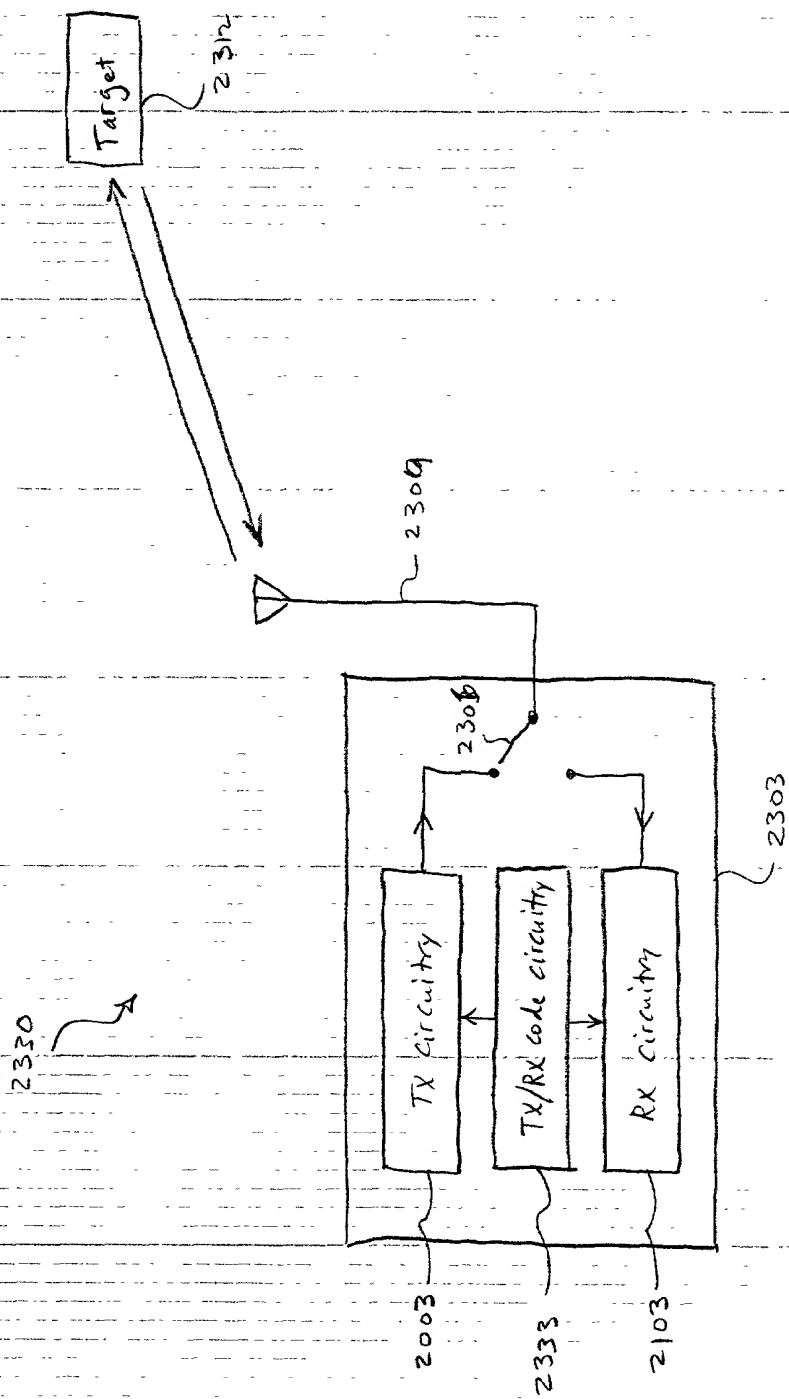
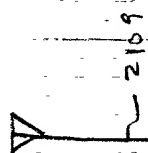
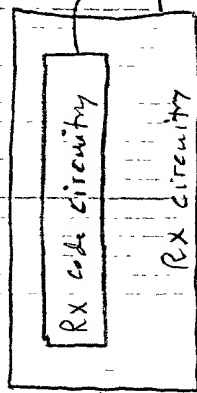


FIG. 29B

2340

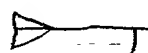


2109

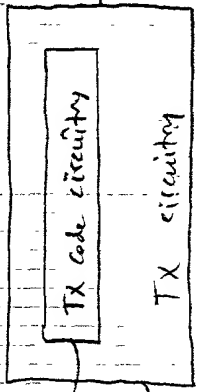


2106

2103



2009



2006

2003

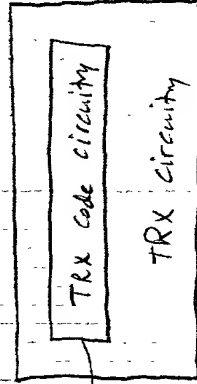
FIG. 30

2350



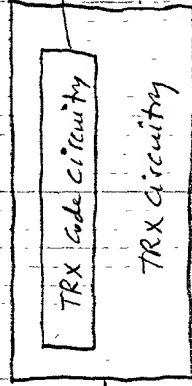
2359A

2362B



2356A

2353A



2356B

2353B

FIG. 31A

2370

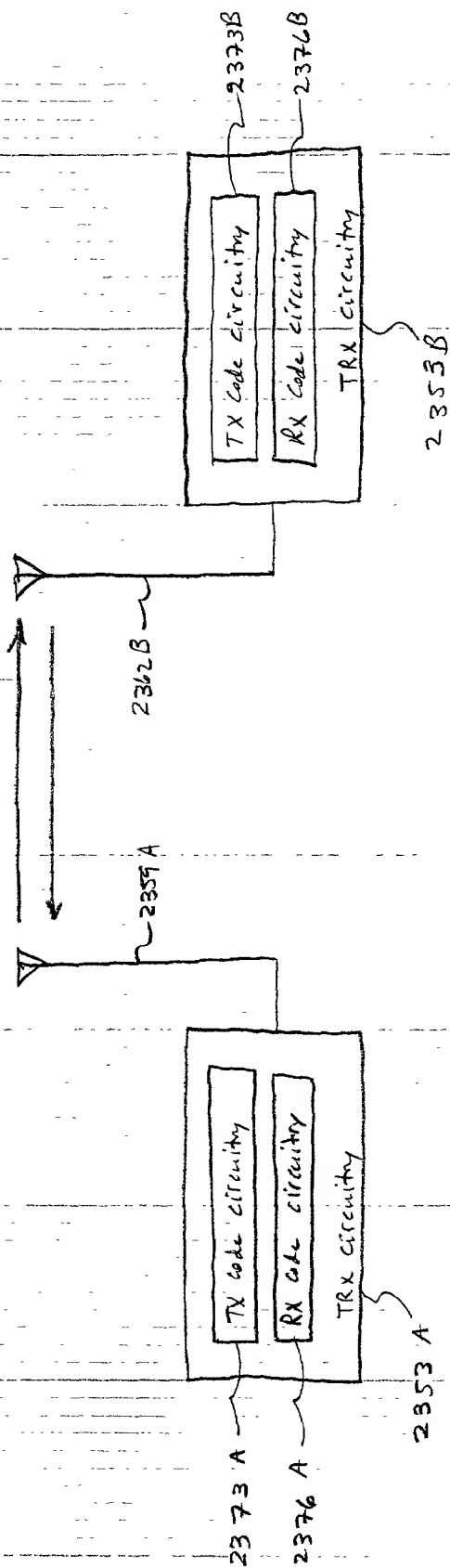


FIG. 31B